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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10033590	12/27/2001	257	778	8112	MAI TRAN

****APPLICANTS:** Su Jin;

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****CONTINUING DATA VERIFIED:** none yet

FOREIGN APPLICATIONS VERIFIED: none yet

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 42390P13376
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Flip-chip opto-electronic circuit		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-99)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE Amount Due Date Paid		Total Claims Print Claim for O.G.	
		DRAWING Sheets Drwg. Figs. Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
<input type="checkbox"/> PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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